08/931,133

5936971b

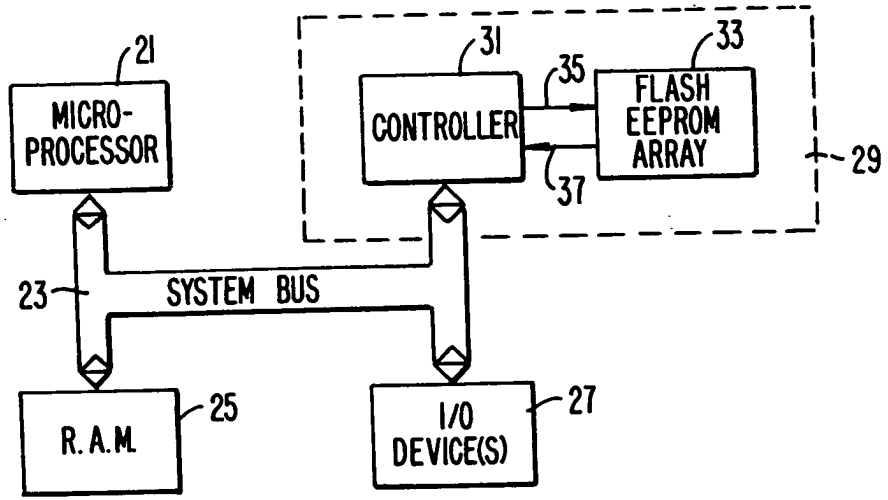
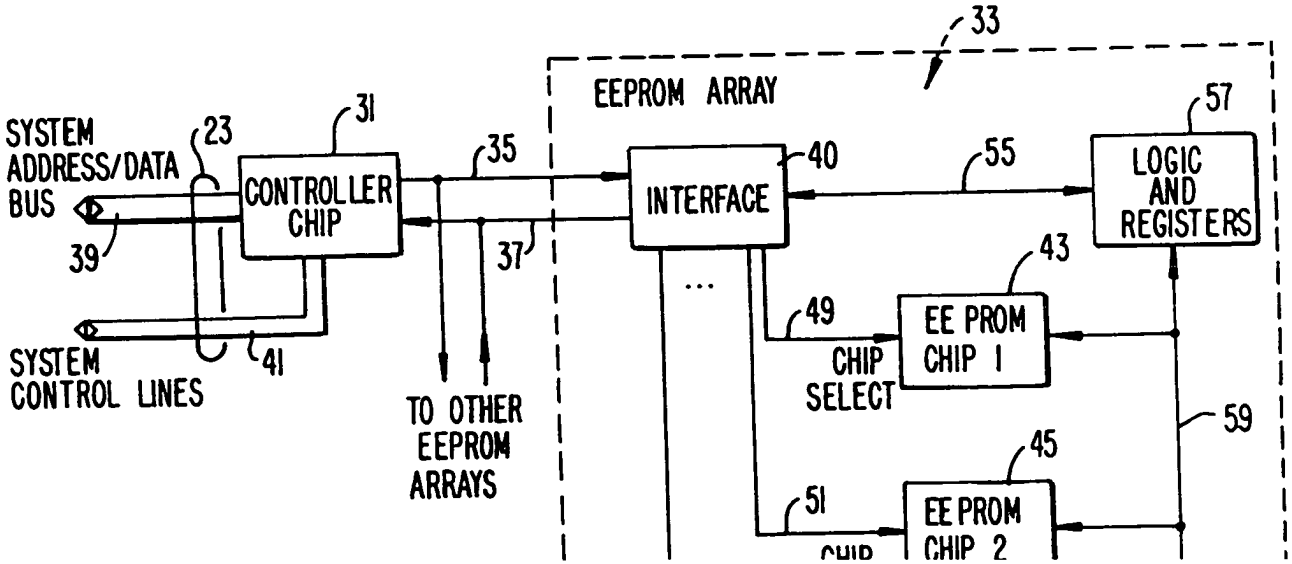


FIG._1A.

8
371 010,200
08/931,133



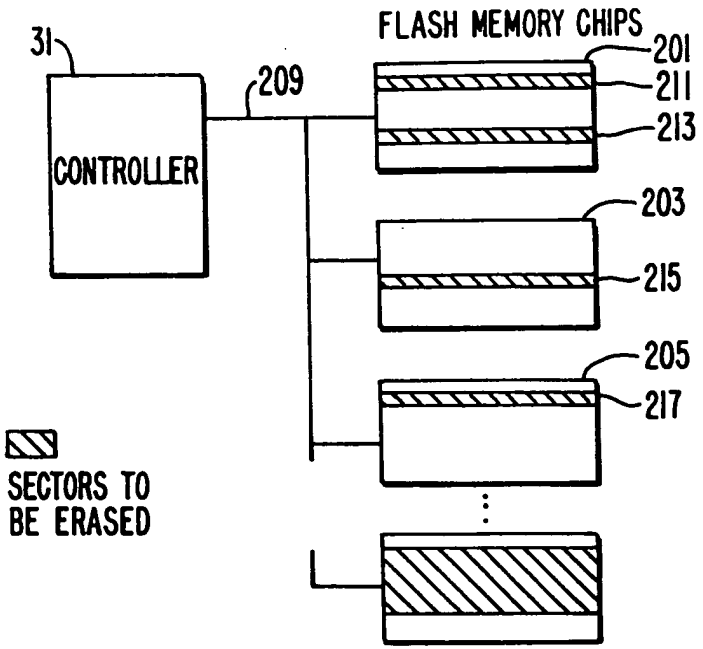

FLASH MEMORY CHIPS
31
CONTROLLER
209
201
211
213
203
215
205
217
SECTORS TO BE ERASED

FIG._2.

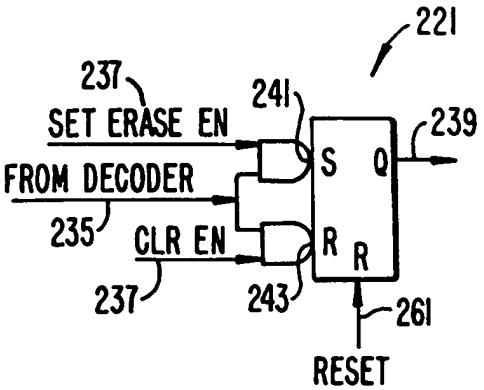
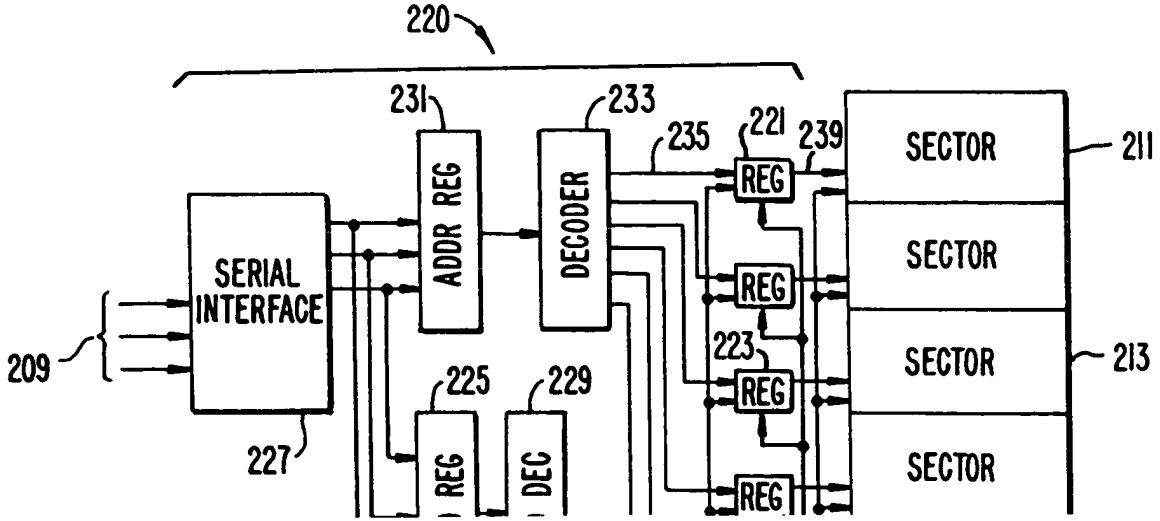
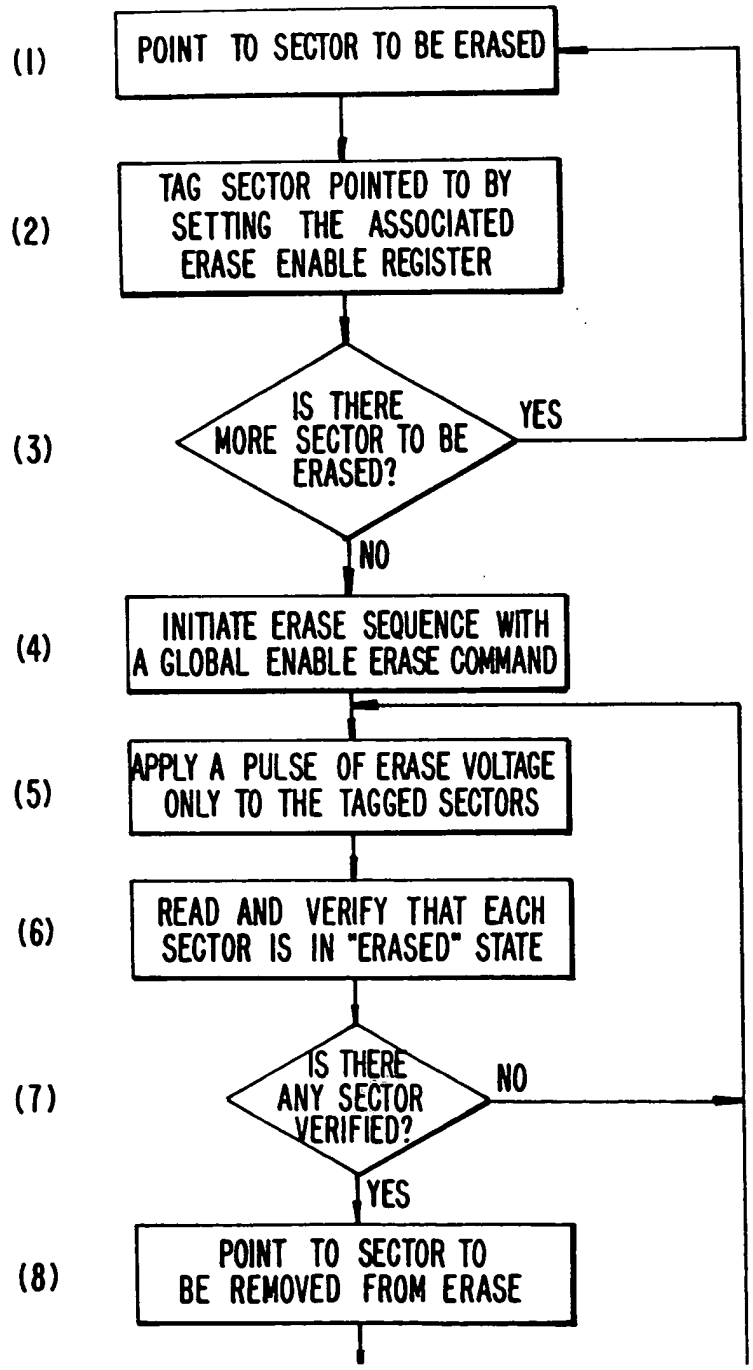
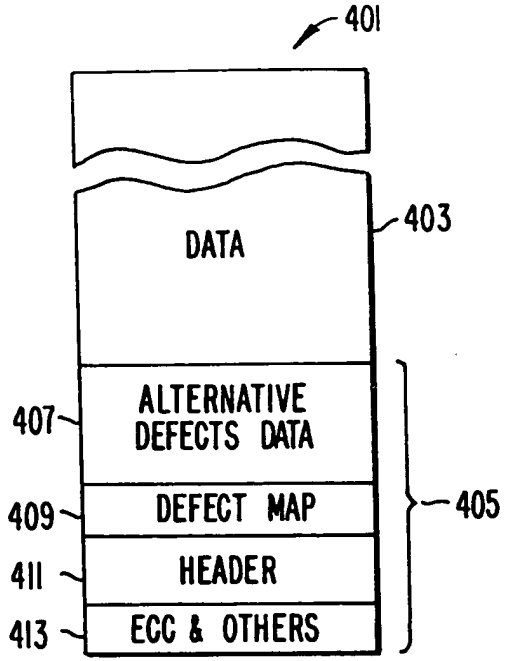

221
237
SET ERASE EN
241
239
FROM DECODER
235
CLR EN
S
Q
R
R
237
243
261
RESET

FIG._3B.

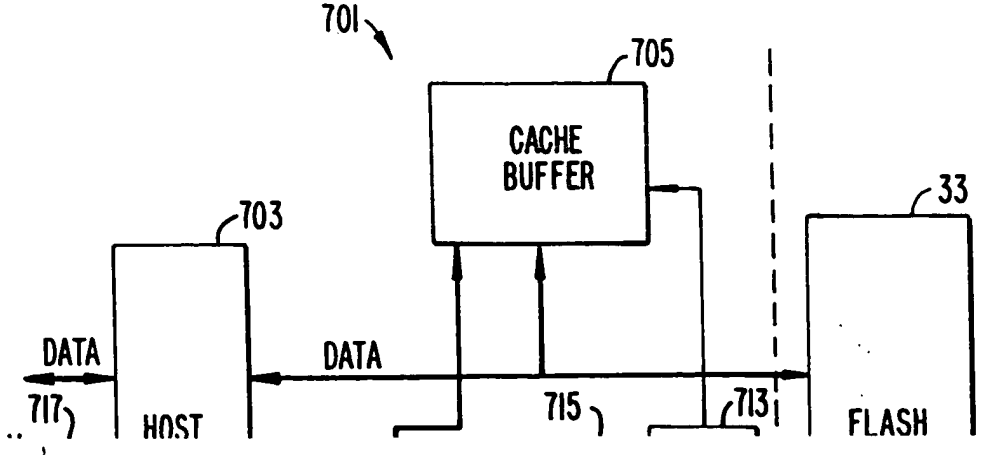

220
231
233
235
221
239
SECTOR
211
SERIAL INTERFACE
ADDR REG
DECODER
REG
SECTOR
209
223
SECTOR
213
225
229
227
REG
DEC
SECTOR

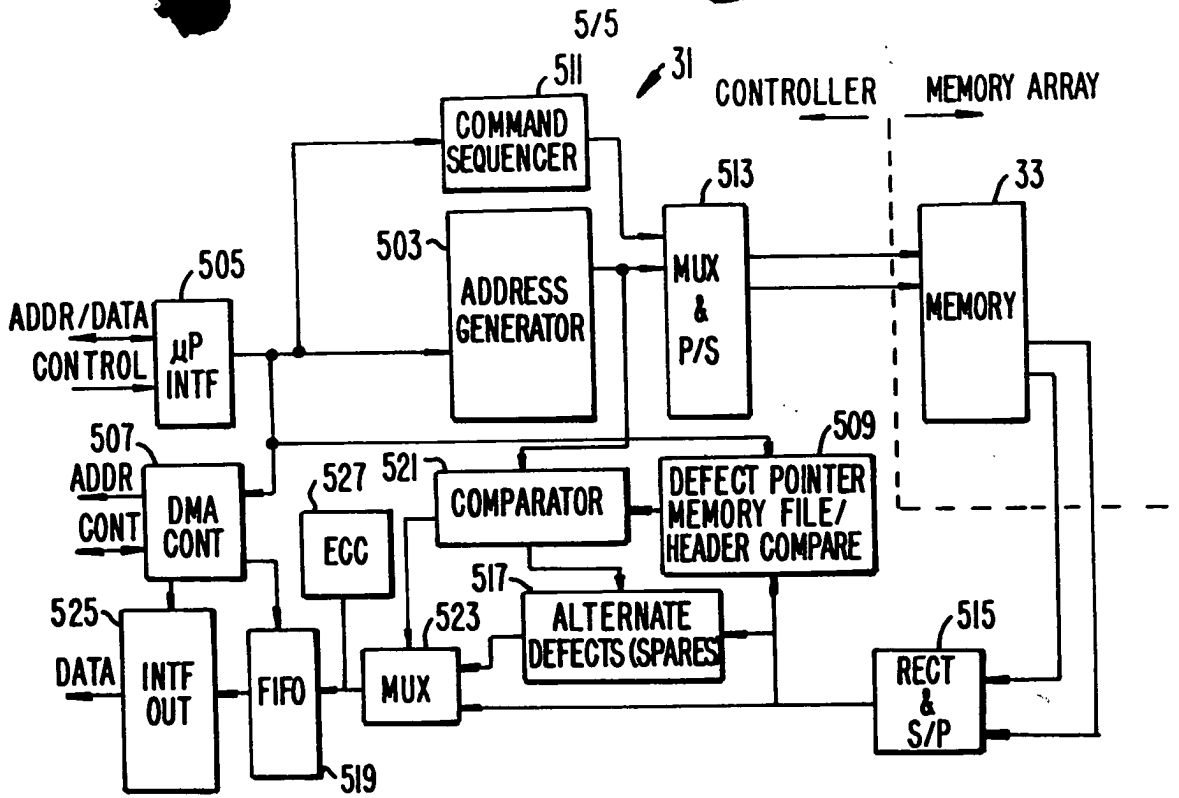

(1)
POINT TO SECTOR TO BE ERASED
(2)
TAG SECTOR POINTED TO BY SETTING THE ASSOCIATED ERASE ENABLE REGISTER
(3)
IS THERE MORE SECTOR TO BE ERASED?
YES
NO
(4)
INITIATE ERASE SEQUENCE WITH A GLOBAL ENABLE ERASE COMMAND
(5)
APPLY A PULSE OF ERASE VOLTAGE ONLY TO THE TAGGED SECTORS
(6)
READ AND VERIFY THAT EACH SECTOR IS IN "ERASED" STATE
(7)
IS THERE ANY SECTOR VERIFIED?
NO
YES
(8)
POINT TO SECTOR TO BE REMOVED FROM ERASE



SECTOR PARTITION

FIG._5.





READ DATA PATH CONTROL

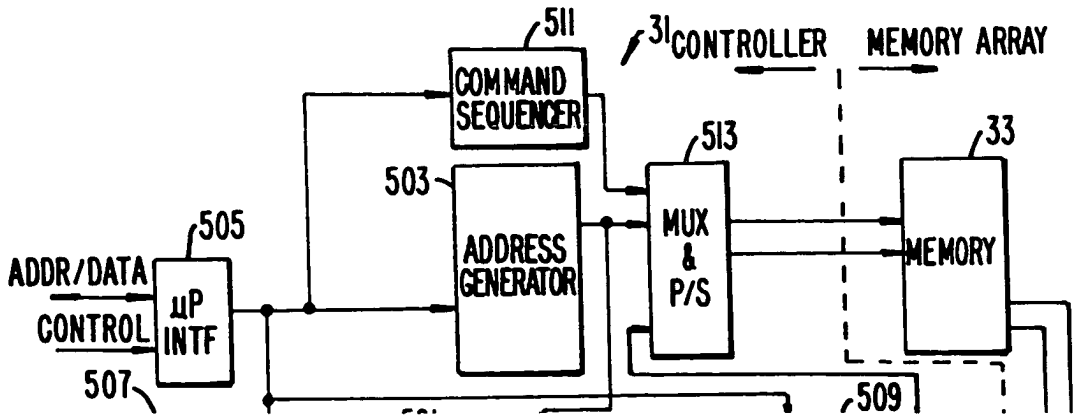



FIG._6.